



Halogens free devices

CHC1084PAGP SERIES

"Low Dropout Linear Regulator

■ FEATURES

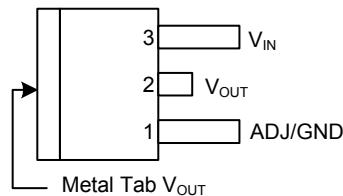
- 1.5V maximum dropout at full load current
- Fast transient response
- Output current limiting
- Built-in thermal shutdown
- Good noise rejection
- 3-Terminal Adjustable or Fixed 1.5V, 1.8V, 2.5V, 3.3V, 5.0V

■ APPLICATIONS

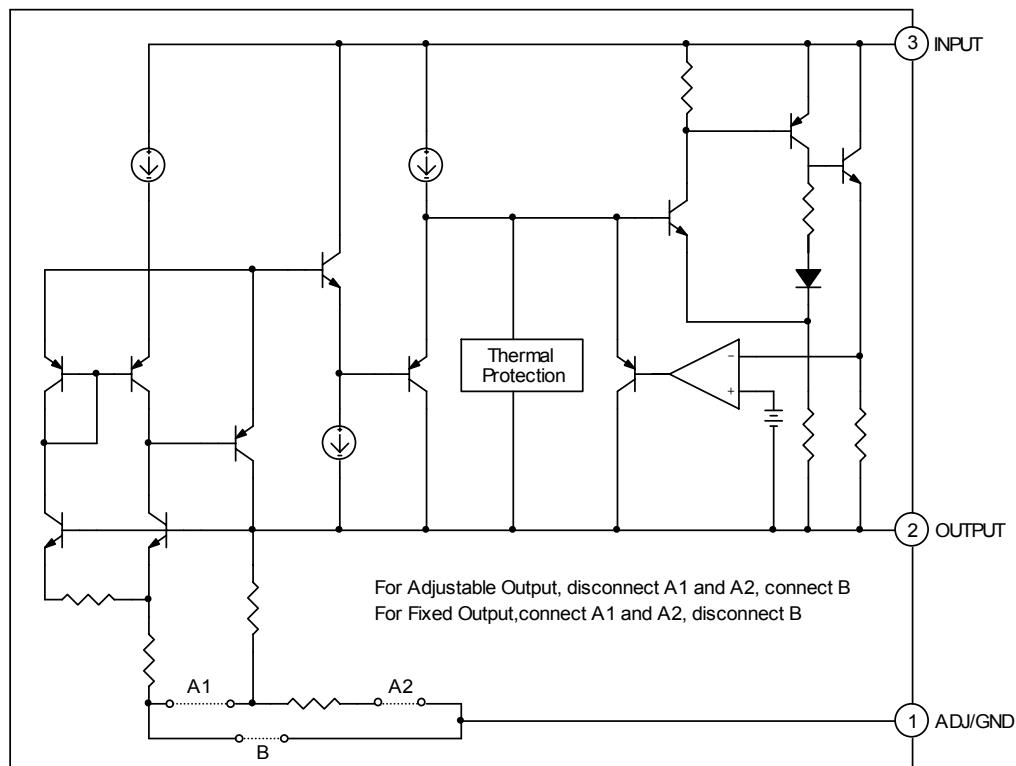
- PC Motherboard
- LCD Monitor
- Graphic Card
- DVD-video player
- NIC/Switch
- Telecom Equipment
- ADSL Modem
- Printer and other Peripheral Equipment

■ PIN CONFIGURATION

TO 252-3L



■ BLOCK DIAGRAM



■ GENERAL DESCRIPTION

The CHC1084PA is a series of low dropout three-terminal regulators with a dropout of 1.5V at 1A output current. The CHC1084PA series provides current limiting and thermal shutdown. Its circuit includes a trimmed bandgap reference to assure output voltage accuracy to be within 2% for 1.5V, 1.8V, 2.5V, 3.3V, 5.0V and adjustable versions. Current limit is trimmed to ensure specified output current and controlled short-circuit current. On-chip thermal shutdown provides protection against any combination of overload and ambient temperature that would create excessive junction temperature.

The CHC1084PA has an adjustable version that can provide the output voltage from 1.25V to 12V with only two external resistors.

The CHC1084PA series is available in the industry standard TO252-3L package.



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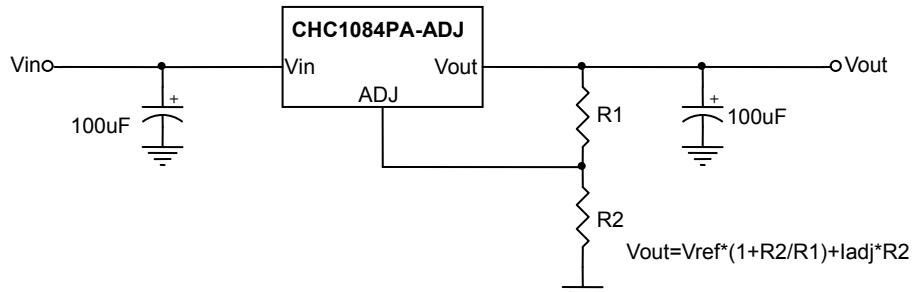
Low Dropout Linear Regulator

ELECTRICAL CHARACTERISTICS (CONTINUED)

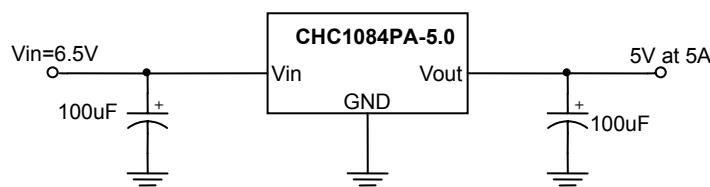
Operating Conditions: $V_{IN} \leq 10V$, $T_J = 25^\circ C$, unless otherwise specified

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Adjust Pin Current		$I_O = 10mA \sim 5A$, $1.5V \leq V_{IN} - V_{OUT} \leq 10V$		60	120	μA
Adjust Pin Current Change		$I_O = 10mA \sim 5A$, $1.5V \leq V_{IN} - V_{OUT} \leq 10V$		0.2	5	μA
Minimum Load Current(ADJ)		CHC1084PA, $1.5V \leq V_{IN} - V_{OUT} \leq 10V$	5	10	ma	
Quiescent Current	I_Q	$V_{IN} = V_{OUT} + 1.25V$		5	10	mA
Long-term Stability		$T_A = 125^\circ C$, 1000hrs		0.3		%
RMS Output Noise (% of V_{OUT})		$T_A = 25^\circ C$, $10Hz \leq f \leq 10kHz$		0.003		%
Power Dissipation	P_D			1.6		W
Thermal Resistance, Junction to Ambient	θ_{JA}			45		/ W
Thermal Resistance, Junction to case	θ_{JC}			2.7		/ W
Thermal Shutdown		Junction Temperature		150		
Thermal Shutdown Hysteresis				25		

TYPICAL APPLICATIONS



The CHC1084PA keeps a constant 1.25V between the output pin and the adjust pin. By placing a resistor R1 across these two pins a constant current flows through R1, adding to the Iadj current and into the R2 resistor producing a voltage equal to the $(1.25/R1) \cdot R2 + I_{adj} \cdot R2$ which will be added to the 1.25V to set the output voltage. This is summarized in the above equation. Since the minimum load current requirement of the CHC1084PA is 10mA, R1 is typically selected to be 121Ω resistor so that it automatically satisfies the minimum current requirement. Notice that since Iadj is typically in the range of 50uA it only adds a small error to the output voltage and should only be considered when a very precise output voltage setting is required. For example, in a typical 3.3V application where R1=121Ω and R2=200Ω the error due to Iadj is only 0.3% of the nominal set point.





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Low Dropout Linear Regulator

■ TYPICAL APPLICATIONS (CONTINUED)

Stability

The CHC1084PA requires the use of an output capacitor as part of the frequency compensation in order to make the regulator stable. For most applications a minimum of 100uF aluminum electrolytic capacitor insures both stability and good transient response.

Thermal Design

The CHC1084PA incorporates an internal thermal shutdown that protects the device when the junction temperature exceeds the maximum allowable junction temperature. Although this device can operate with junction temperatures in the range of 150°C, it is recommended that the selected heat sink be chosen such that during maximum continuous load operation the junction temperature is kept below the temperature.

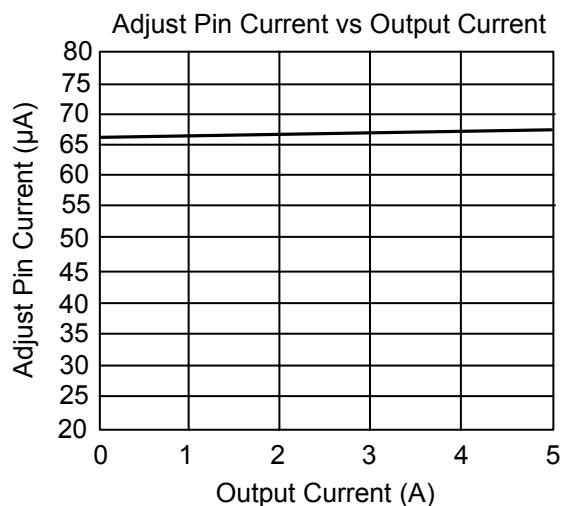
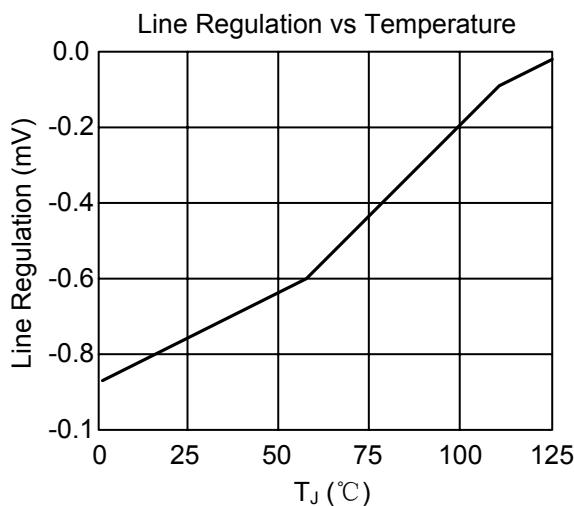
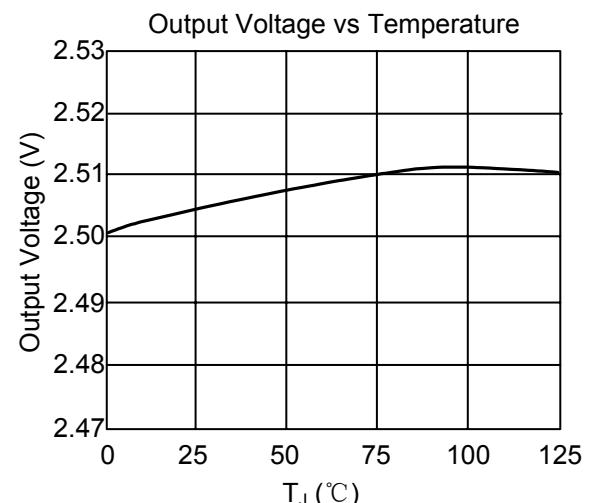
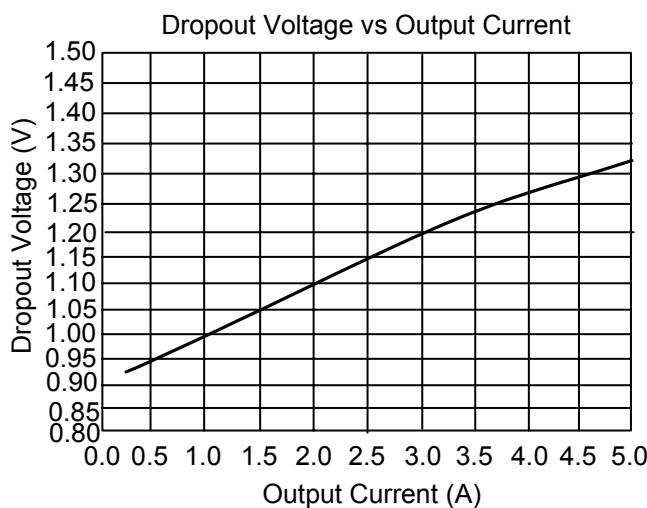
$$P_d = V_{out} \cdot I_{out}$$

$$T_j = T_A + P_d \cdot \theta_{JA} < 150^\circ\text{C}$$

Layout Consideration

The output capacitors must be located as close to the V_{out} terminal of the device as possible. It is recommended to use a section of a layer of the PC board as a plane to connect the V_{out} pin to the output capacitors to prevent any high frequency oscillation that may result due to excessive trace inductance.

■ TYPICAL PERFORMANCE CHARACTERISTICS



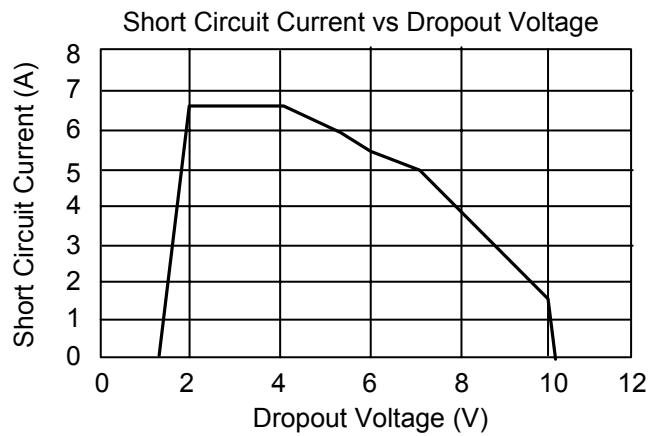
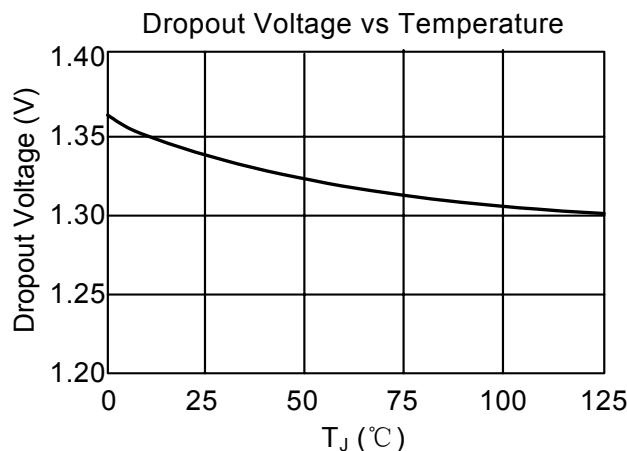


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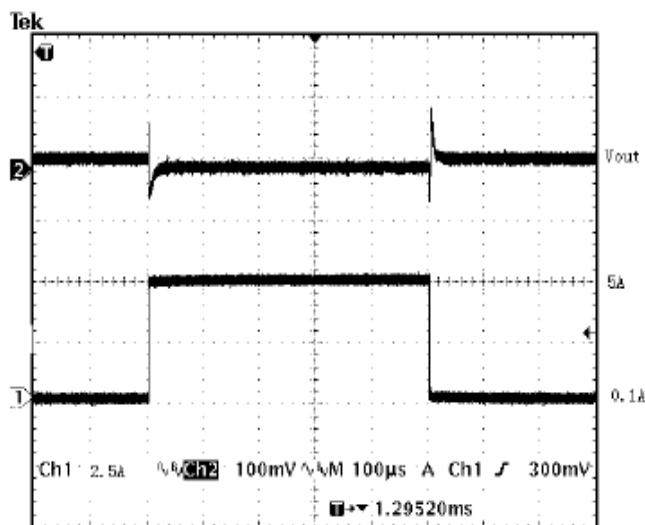
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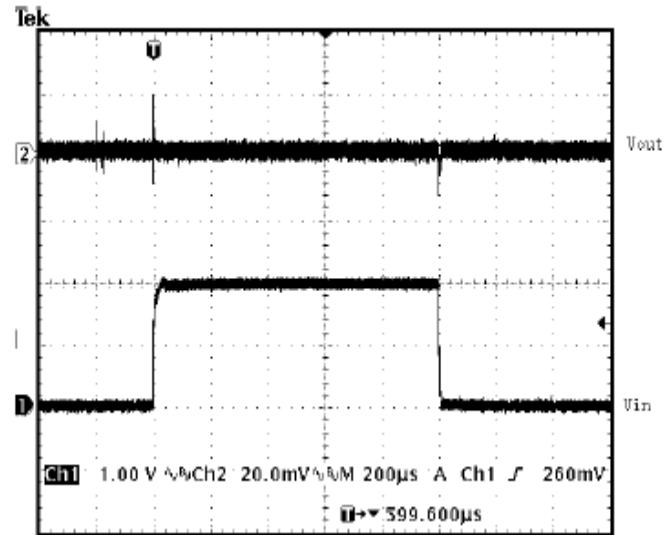
■ TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)



Load Transient Response



Line Transient Response



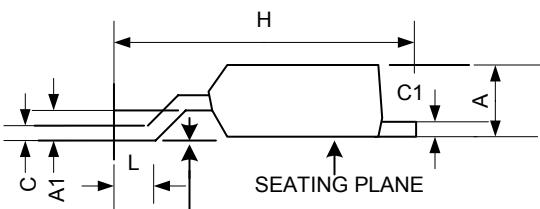
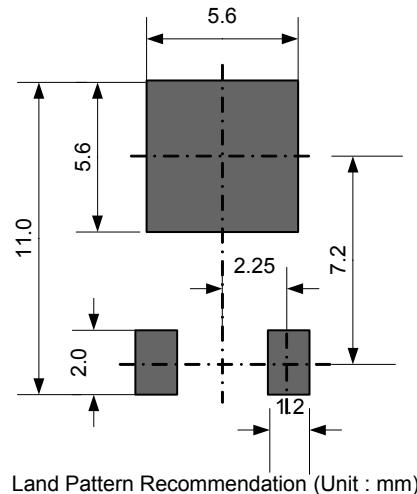
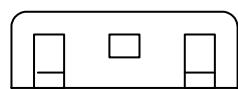
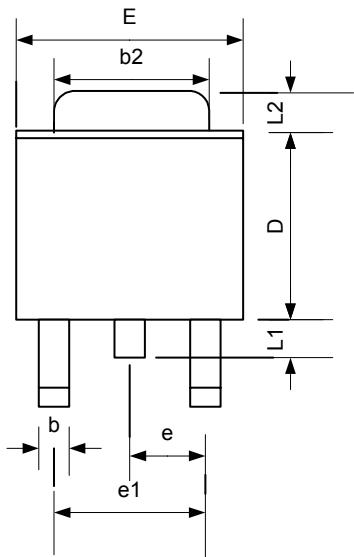


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Low Dropout Linear Regulator

TO252-3L



SEE NOTE2

Notes:

1. JEDEC Outline: TO-252 AB
2. Mils suggested for positive contact at mounting

Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	2.18	2.29	2.38	0.086	0.090	0.094
A1	1.08	1.15	1.32	0.043	0.045	0.052
b	0.64		0.78	0.025		0.031
b2	5.23	5.35	5.43	0.206	0.211	0.214
C	0.51TYP.			0.020TYP.		
C1	0.46	0.52	0.58	0.018	0.020	0.023
D	5.33	5.57	5.80	0.210	0.219	0.228
E	6.38	6.58	6.68	0.251	0.259	0.263
e	2.24		2.34	0.088		0.092
e1	4.48		4.68	0.176		0.184
H	9.00	9.70	10.40	0.354	0.382	0.409
L	0.51			0.020		
L1	0.65	0.83	0.95	0.026	0.033	0.037
L2	1.52	1.78	2.03	0.060	0.070	0.080