



CHENMKO ENTERPRISE CO.,LTD

Halogens free devices

SURFACE MOUNT
NPN Digital Silicon Transistor

VOLTAGE 50 Volts CURRENT 100 mAmpere

CHDTC115GKGP

APPLICATION

* Switching circuit, Inverter, Interface circuit, Driver circuit.

FEATURE

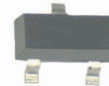
- * Small surface mounting type. (SOT-23)
- * High current gain.
- * Suitable for high packing density.
- * Low collector-emitter saturation.
- * High saturation current capability.
- * Internal isolated NPN transistors in one package.
- * Built in bias resistor(R1=100kΩ, Typ.)

CONSTRUCTION

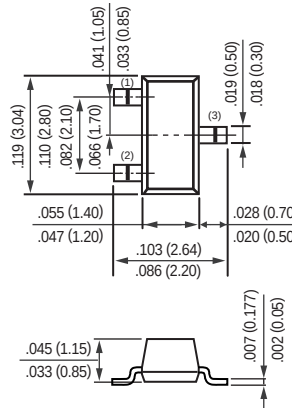
* One NPN transistors and bias of thin-film resistors in one package.

MARKING

GKD



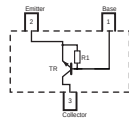
SOT-23



Dimensions in millimeters

SOT-23

CIRCUIT



LIMITING VALUES

In accordance with the Absolute Maximum Rating System.

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-Base voltage		50	V
V _{CEO}	Collector-Emitter voltage		50	V
V _{EB0}	Emitter-Base voltage		5	V
I _c	Collector current		100	mA
P _c	Collector Power dissipation	T _{amb} ≤ 25 °C, Note 1	200	mW
T _{STG}	Storage temperature		150	°C
T _j	Junction temperature		-55 +150	°C
RθJ-S	Thermal resistance , Note 1	junction - soldering point	140	°C/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

RATING CHARACTERISTIC (CHDTC115GKGP)

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
BV _{CBO}	Collector-Base breakdown voltage	I _C = 50uA	50.0	–	–	V
BV _{CEO}	Collector-Emitter breakdown voltage	I _C = 1mA	50.0	–	–	V
BV _{EBO}	Emitter-Base breakdown voltage	I _E = 72uA	5.0	–	–	V
V _{CE(sat)}	Collector-Emitter Saturation voltage	I _C = 5mA; I _B = 0.25mA	–	–	0.3	V
I _{CBO}	Collector-Base current	V _{CB} = 50V	–	–	0.5	uA
I _{EBO}	Emitter-Base current	V _{EB} = 4V	–	–	58	uA
h _{FE}	DC current gain	I _C = 5mA; V _{CE} = 5.0V	82	–	–	
R ₁	Input resistor		70	100	130	KΩ
f _T	Transition frequency	I _E =-5mA, V _{CE} = 10.0V f=100MHz	–	250	–	MHz

Note

1.Pulse test: t_p≤300uS; δ ≤0.02.