



CHENMKO ENTERPRISE CO.,LTD

Halogens free devices

SURFACE MOUNT
PNP Digital Silicon Transistor

VOLTAGE 50 Volts CURRENT 30 mAmpere

CHDTA144WEGP

APPLICATION

* Switching circuit, Inverter, Interface circuit, Driver circuit.

FEATURE

- * Small surface mounting type. (SC-75/SOT416)
- * High current gain.
- * Suitable for high packing density.
- * Low collector-emitter saturation.
- * High saturation current capability.
- * Internal isolated PNP transistors in one package.
- * Built in bias resistor(R1=47kΩ, Typ.)

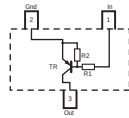
CONSTRUCTION

* One PNP transistors and bias of thin-film resistors in one package.

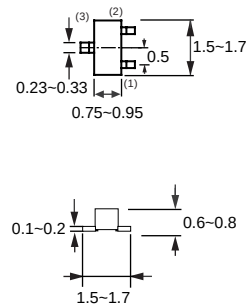
MARKING

WE2

CIRCUIT



SC-75/SOT-416



Dimensions in millimeters

SC-75/SOT-416

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CC}	Supply voltage		—	-50	V
V _{IN}	Input voltage		-40	+10	V
I _O	DC Output current		—	-30	mA
I _{C(Max.)}			—	-100	
P _{TOT}	Total power dissipation	T _{amb} ≤ 25 °C, Note 1	—	150	mW
T _{STG}	Storage temperature		-55	+150	°C
T _J	Junction temperature		—	150	°C
R _{θJ-S}	Thermal resistance	junction - soldering point	—	140	°C/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

RATING CHARACTERISTIC (CHDTA144WEGP)

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{I(off)}	Input off voltage	I _O =-100uA; V _{CC} =-5.0V	-0.8	—	—	V
V _{I(on)}	Input on voltage	I _O =-2mA; V _O =-0.3V	—	—	-4.0	V
V _{O(on)}	Output voltage	I _O =-10mA; I _I =-0.5mA	—	- 0.1	- 0.3	V
I _I	Input current	V _I =-5V	—	—	-0.16	mA
I _{C(off)}	Output current	V _I =0V; V _{CC} =-50V	—	—	-0.5	uA
h _{FE}	DC current gain	I _O =-5mA; V _O =-5.0V	56	—	—	
R ₁	Input resistor		32.9	47	61.1	KΩ
R ₂ /R ₁	Resistor ratio		0.37	0.47	0.57	
f _T	Transition frequency	I _E =5mA, V _{CE} =-10.0V f=100MHz	—	250	—	MHz

Note

1.Pulse test: t_p≤300uS; δ≤0.02.